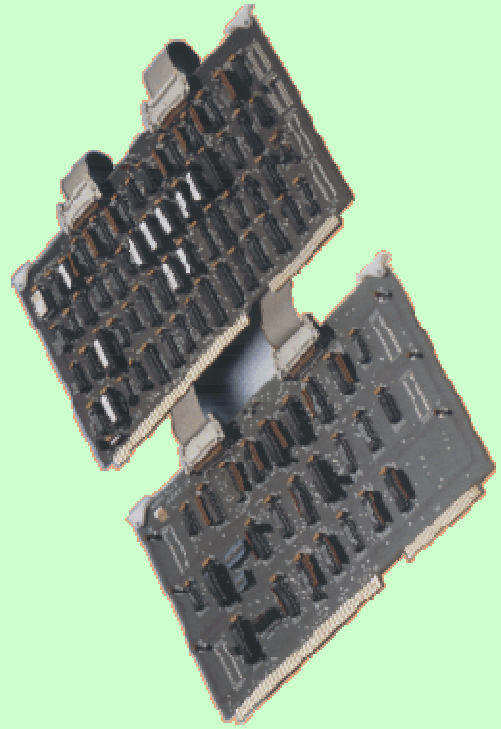




Features

- ✓ Interface between a MULTIBUS I rack and from one to four MULTIBUS I racks
Interface entre un châssis MULTIBUS I et de un à quatre châssis MULTIBUS I
- ✓ 4, 8, 16 or 32K.byte window in I/O
Fenêtre de 4, 8, 16 ou 32K.octets en I/O
- ✓ 4Kb to 2Mb window in memory
Fenêtre de 4Ko à 2Mo en mémoire
- ✓ 8 or 16-bit exchanges
- ✓ Transfer rate $\geq 1M$.words/s
- ✓ Interrupts management
Gestion des interruptions
- ✓ Interrupt send to receiver
Emission d'une interruption vers récepteur
- ✓ Multiprocessor structure
Structure multiprocesseurs
- ✓ Easy to use
Utilisation aisée
- ✓ MULTIBUS I / A24 standard



Description

Developed for industrial environments, the **MULTIBUS I / MULTIBUS I** gateway enables dialogue between several racks of the same standard. It represents a "hardware" local network with a very high transfer rate between a **MULTIBUS I** "TRANSMITTER" rack and one to four **MULTIBUS I** "RECEIVERS".

All the resources of the **MULTIBUS I** are fully preserved and each rack can have its own resources.

The concept enables simple and powerful extension of a rack, where more than 80 **MULTIBUS I** boards can be in relation.

*Développée pour l'environnement industriel, la passerelle **MULTIBUS I / MULTIBUS I** assure le dialogue entre plusieurs châssis de même standard. Elle représente un réseau local "matériel" à taux de transfert très élevé, entre un châssis **MULTIBUS I** "EMETTEUR" et de un à quatre châssis **MULTIBUS I** "RECEPTEUR".*

*Toutes les ressources du **MULTIBUS I** sont intégralement préservées et chaque châssis peut posséder ses propres ressources.*

*Le concept offre une mise en oeuvre simple et puissante d'extension d'un châssis, où plus de 80 cartes **MULTIBUS I** peuvent être en relation.*

PMM 801

SPECIFICATIONS

(t = 25°C)

TYPE	MULTIBUS I / MULTIBUS I GATEWAY
MULTIBUS I TRANSMITTER <ul style="list-style-type: none">- Type- Configuration- Space decoded- Memory exchange window- I/O window- Space decoded in I/O- Space decoded in memory- MULTIBUS I exchanges	Slave board on the MULTIBUS I In the I/O space 16 bytes 4Kb to 2Mb in the MULTIBUS I memory space 4 to 32K.bytes 16 address lines 24 address lines 8 or 16 bits
MULTIBUS I RECEIVER <ul style="list-style-type: none">- Type- Multiprocessor management- Standard- Decoding- Bus clock- Transfers- Mode- Interrupt to the transmitter MULTIBUS I- Interrupts- IT generation	Controller board on the MULTIBUS I Yes, with backplane provided with parallel priorities MULTIBUS I A24/D16 10MHz 8 or 16 bits RWD (release after each exchange) MULTIBUS I timeout Management of 8 interrupt levels to one receiver level Generation of an interrupt on one of the 8 levels of the MULTIBUS I
EXCHANGES <ul style="list-style-type: none">- Transfers- Rate- Communication support- Maximum connecting cable length- Couplers	8 or 16 bits 1M.words/s (2M.bytes) 2 micro-ribbon cables, 50 pins adapted (l = 1.5 m) 15m One MULTIBUS board can manage four MULTIBUS I racks
PRESENTATION (in mm) <ul style="list-style-type: none">- MULTIBUS I boards	305 x 171.5 x 10
ENVIRONMENT <ul style="list-style-type: none">- Operating temperature- Storage temperature- Relative humidity	0°C to + 60°C - 10°C to + 70°C 90 % (without condensation)

HOW TO ORDER?

PMM 801

KIT COMPRISING :

1 **MULTIBUS I** "transmitter" board Ref. : EM1M1
1 **MULTIBUS I** "receiver" board Ref. : RM1M1

Ribbon WR 310/XXX ⇒ Order separately

Length (in mm) of the ribbon